

Fig.1

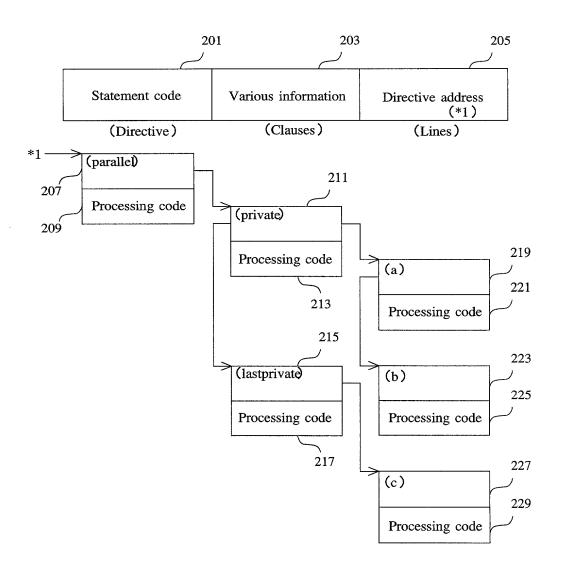


Fig.2

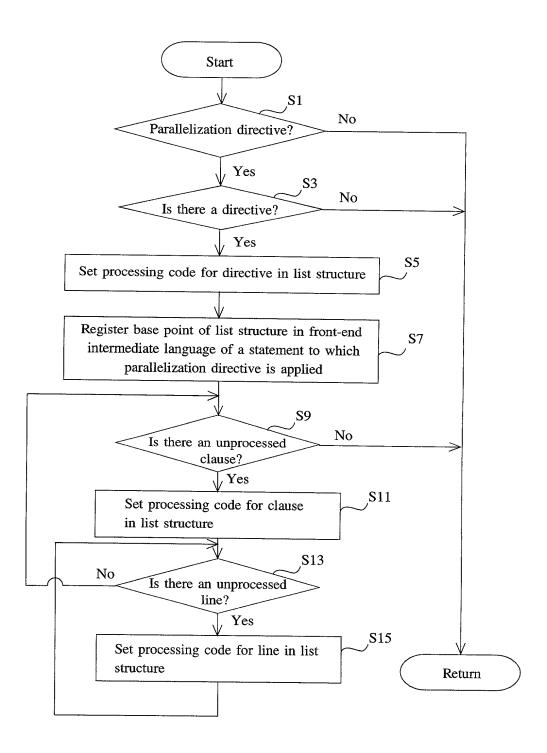


Fig.3

		401	403	
	Processing	Processing 1	Processing 2	
	Processing code		<i>5</i>	
405	parallel	al	b1	
407	private	a2	b2	
409	variable	a3	b3	
411	lastprivate	a4	b4	
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Fig.4

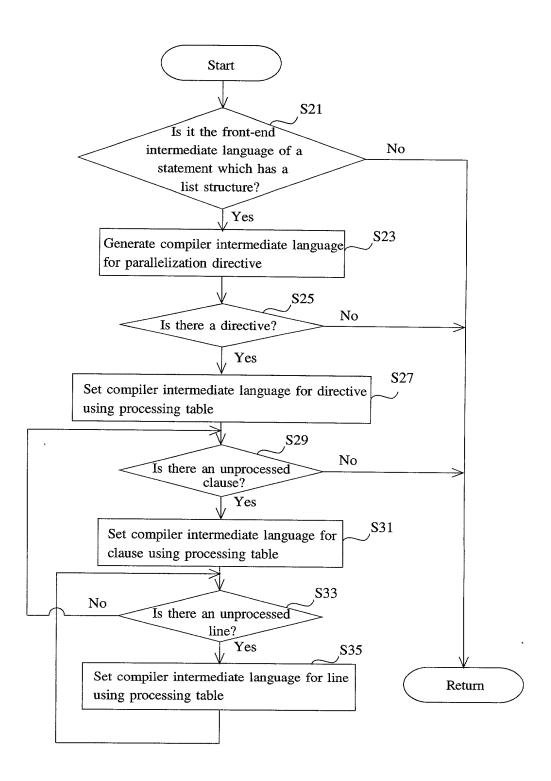


Fig.5